

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS:

1. (Once Amended) A method for accessing memory in a multiprocessor system, **[the method]** comprising:

from a requesting processor, issuing a request for **[a block of]** data to one or more other processors and memory, each copy of the **[block of]** data being associated with a state information indicating whether the copy is valid or not;

in each of the processors and memory that receive the request, checking to determine whether a valid copy of the **[block of]** data exists; and

returning **[a] the** valid copy of the requested data from one of the other processors or memory such that only the processor or memory having the valid copy of the data **[block]** responds to the request.

9. (Once Amended) A multiprocessor system comprising:

two or more processors, each in communication with a shared memory via a memory controller;

the processors in communication with the memory controller for issuing a request for **[a block of]** data, each of the processors and the shared memory being capable of storing a copy of the requested **[block of]** data, and each copy of the requested **[block of]** data being associated with state indicating whether the copy is valid or invalid,

each of the processors and the shared memory being responsive to a request to check itself for a valid copy of a requested **[block] data** such that only the processor or the shared memory having the valid copy responds to the request for the requested **[block] data**.

19. (Once Amended) A multiprocessor system comprising:

two or more processors, each in communication with a shared memory;

the processors in communication with the shared memory for issuing a request for [a **block of**] data, each of the processors and the shared memory being capable of storing a copy of the requested [b**lock of**] data, and each copy of the requested [b**lock of**] data being associated with state indicating whether the copy is valid or invalid,

each of the processors and the shared memory being responsive to a request to check itself for a valid copy of a requested [b**lock**] data such that only the processor or shared memory having the valid copy responds to the request for the requested [b**lock**] data.